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Yeshwantrao Chavan College of Engineering was established in 1983 by Late Shri Bapuji Deshmukh. The College is recognized by the AICTE and permanently affiliated to the Rashtrasant Tukadoji Maharaj Nagpur University, Nagpur. The institute is located at Sevagram, Dist. Wardha. The college is running seven engineering undergraduate programs (BE) & five engineering post-graduate programs (M.Tech) along with postgraduate program in management studies (MBA). The institute is approved as Research Center for PhD and ME by Research. 153 research scholars are registered for PhD at our research centre and 27 students for higher learning studies (ME by Research). The institute has been awarded the Vidyashree Village Industries Commission (VIC) project grant by Government of India, New Delhi. Eco-friendly Green Campus is spread over 32 acres. The institute maintains qualified and experienced staff for academic excellence. Well equipped & Internet-ready laboratories are available to satisfy the recent global needs. This institute is an approved Local Nodal Center to promote Outcome Based NBA Accreditation Process. During last 33 years, the institute marched ahead with a promise of academic excellence for overall development of its students and further promises to strive hard to keep the same atmosphere intact for years to come. Institute has a registered alumni Association named as "Gandhigram Engineers' Alumni Association". 

About Sevagram
Sevagram is situated around 6 km from Wardha city. Mahatmaji and Bas resided here from 1937 onwards. Bapu Kuti is at 1 km away from the college.

Preamble

The primary goal of the conference is to promote research and development activities in the field of engineering science and technology to bring researchers and experts from academia and industry on a common platform to discuss the recent technologies being adopted in India and abroad. The conference is organized in association with The Institution of Electronics and Telecommunication Engineers (IETE), Computer Society Of India (CSI), International Journal of Computer Applications ([IJCAI]ONLINE) (ISSN: 0975-8887), International Journal of Journal of Research &Technology (IERT) (ISSN: 2278-0181) and The Institution of Engineers (India).

Objectives

- To explore research trends in various streams of engineering, science and technology.
- To provide a platform to all delegates to share views, knowledge and achievements in the field of Engineering, Science & Technology.
- To encourage the delegates for technical writing and presentation based on their original research work.
- Identification and promotion of R&D activities at the individual as well as at the institutional level.

Thrust Areas

**Track 1 – Electronics Engineering (ETRX)**

**Track 2 – Electronics & Telecommunication Engineering (EEXT)**
- Emerging & Innovative Technology for Digital India and for Make in India, Reconfigurable System Level VLSI for DSPASIC & SOC CAD for VLSI RF Circuit Design, Bio-MEMS/NEMS/MMIC & CMOS Circuits & Biomedical Sensors & Instrumentation, Cryptography, Medical Electronics & Bioinformatics, Coding Techniques for 3G/4G/LTE

**Track 3 – Electronics & Power Engineering (EXPO)**

**Track 4 – Computer Science & Engineering (CSE)**

**Track 5 – Mechanical Engineering (MECH)**

**Track 6 – Civil Engineering (CIVIL)**

**Track 7 – Master of Business Administration (MBA)**

**Track 8 – Basic Sciences (BS)**

The Conference deliberations are being structured to cover the following thrust areas (but not limited to):

The Conference deliberations are being structured to cover the following thrust areas (but not limited to):

**Track Chairs**

- Track 1: Prof. M. M. Chore
- Track 2: Prof. A. B. Bagde
- Track 3: Prof. Mrs. S. R. Bhawarkar
- Track 4: Prof. Ms. V. G. Bhujade
- Track 5: Prof. Ms. C. M. Meshram
- Track 6: Prof. S. M. Fatmal
- Track 7: Prof. M. R. Nikhar
- Track 8: Prof. R. B. Gajbhye
- Track 9: Dr. Mrs. A. M. Chaudhary

**About BDCE**

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